REMARKS

Claims 1-24 are pending in the application. Claims 1 and 13 are amended herein. No claims have been allowed.

Prior Art Citations

Applicants respectfully request consideration of the prior art cited by the Applicants on November 2, 2004. Applicants further request the return of the forms PTO/SB/08A initialed to indicate the Examiner's consideration of the prior art.

Claim Objections

Claims 1 was objected to because of an informality in the preamble. Applicants thank the Examiner for pointing out the redundant appearance of "a method for". The indicated correction has been made. Applicants respectfully request withdrawal of the objection.

Rejections under 35 U.S.C. § 103

Claims 1-24 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Bose et al. (U.S. Patent No. 6,654,428), hereinafter "Bose", in view of Hardin et al. (U.S. Patent Publication No. US 2002/0099753, hereinafter "Hardin".

Applicants respectfully submit that the claims are patentable over the cited art. Bose discloses systems and methods for wireless communication including integrating wireless receivers and transmitters with host computer platforms. The systems and methods include a data access channel that delivers into the memory space of an application program digital data that is representative of a base band modulated signal. (Abstract). In particular, Bose teaches a system and method for a software radio. It is an object of Bose to provide wireless telecommunication devices that can adapt or alter the signal processing being carried out by the wireless communication device. (column 2, lines 27-40).

Applicants respectfully submit that the teaching of Bose is not in an analogous area of art to the claimed invention. Bose teaches the transfer of data to a host platform and an application process that performs signal processing on the data. The storage of the

transferred data is also described. (column 8, lines 25-53). Bose teaches a conventional peripheral component interface (PCI) bus, not a software bus as taught and claimed in the present application. Bose does not teach multiple end user applications as in the present application, but rather a signal processing application for a software radio.

The sections of Bose cited by the Examiner will now be analyzed for the purpose of better explaining the differences between Bose and the claimed invention, and thus why the claimed invention is patentable over the cited art. The Office Action states that Bose includes:

> creating a plurality of bus listener objects in an object framework of the device defining a plurality of bus addresses, each corresponding to one and only one of the plurality of bus listener objects (i.e., reads on virtual memory address and physical memory address); receiving a value from a process; writing the value in a bus address (i.e., reads on swapping of buffers and affecting a transfer into the application data space by altering the page table within the virtual memory system) (col. 9, lines 1-28 and lines 36-61);...

Applicants respectfully disagree with the above reading of Bose. For example, nowhere does Bose disclose or suggest creating a bus listener object in an object framework as in the present specification and claims. Nor does Bose disclose or suggest defining a plurality of bus addresses. Remember that Bose does not disclose a software bus, but rather a hardware bus (PCI bus). Even so, Bose does not even disclose bus listeners of the hardware variety.

At the indicated sections of column 9, Bose describes a conventional memory scheme including page tables (see also the reference to the Bach publication at column 9, line 30). The addresses in such a memory architecture are not the same as either physical or software bus addresses. Bose simply does not disclose defining a plurality of bus addresses as claimed.

The Office Action further states:

> a bus listener object to which the bus address corresponds responding to a change in value stored in the bus address by invoking an object method associated with the address, wherein a plurality of relationships between the plurality of bus listener objects, the plurality of bus addresses, and a plurality of object methods is defined by the control file (i.e., reads on [sic] interface card acting as a PCI bus master which initiates transfers on

the PCI bust [sic] and controls swapping of buffers and affecting a transfer into application data space by altering the page table within the virtual memory system) (col. 9, lines 36-61 and col. 13, lines 45-67).

Applicants respectfully disagree with that the above interpretation of Bose. Bose does not teach bus listener objects. Bose does not teach a bus listener object responding to a change in value stored in a bus address. Bose does not teach values stored in bus addresses as claimed and disclosed. Bose does not teach invoking an object method associated with the address when a value stored in a bus address is changed. Bose does not teach invoking any object methods or any methods. Bose teaches only storing data to be operated on by a signal processing application in a memory architecture with page tables. The data being stored in the conventional memory in Bose does not invoke methods.

Further, the interface card on the PCI bus is not analogous to, nor does it suggest a control file as disclosed and claimed. A PCI interface card is a hardware device to control access to a hardware bus. Bose does not teach a software bus as disclosed and claimed. For that reason, Bose simply does not teach a plurality of relations between a plurality of bus listener objects, a plurality of bus addresses, and a plurality of object methods defined by a control file as claimed.

The Office Action cites column 13, lines 45-67. Applicants respectfully submit that this section of Bose illustrates how the teaching of Bose does not really apply to a programming environment, applications, or an object-oriented framework. Rather, these things are mentioned as existing to facilitate the development of signal processing programs, such as the cellular receiver process 80. Applications and application programming and development are peripheral to the teaching of Bose, and the particular citation in column 13 provides no teaching or suggestion relevant to the claimed invention.

For all of the foregoing reasons, Applicants respectfully submit that the invention of claims 1 or 13 would not have been obvious to one of ordinary skill in the art in view of Bose, taken alone or with other references.

The Office Action states that Bose does not explicitly disclose storing, on an electronic device, an application set and an associated control file wherein the control file

integrates a plurality of applications in the application set, etc. Hardin is cited as disclosing storing, on an electronic device, and application set and an associated control file wherein the control file integrates a plurality of applications in the application set such that more than one application can execute on the electronic device concurrently, and transparently to a user of the electronic device. Applicants respectfully disagree that the cited paragraphs of Hardin disclose or suggest a control file or storing a control file as claimed. Rather, the cited paragraphs state that some applications exist wherein it is desirable to concurrently run multiple virtual machines on a single processor, and that one motivation for using multiple virtual machines is to enable the implementation of different policies for different applications. Applicants fail to see how teaching regarding multiple virtual machines is relevant to the claims. Hardin simply does not disclose a control file as claimed.

Applicants respectfully submit that Hardin does not make up the deficiencies of Bose. The combination of Bose and Hardin fails to disclose or suggest at least storing, on the electronic device, an application set and an associated control file, wherein the application set includes at least one application comprising a plurality of object methods, wherein the control file integrates a plurality of applications in the application set such that more than one application can execute on the electronic device concurrently, and transparently to a user of the electronic device, as in claim 1.

Applicants further submit that one of ordinary skill in the art would not be motivated to combine the references as suggested. For example, one of ordinary skill would not look to Bose, which provides teaching regarding software radios and digital signal processing, to solve a problem related to executing multiple applications on an electronic device concurrently. In addition, one of ordinary skill would not look to Hardin for teaching regarding a control file that integrates a plurality of applications in an application set such that more than one application can execute on the electronic device concurrently, and transparently to a user of the electronic device, as in claim 1. For example, Hardin is a disclosure regarding concurrently supporting multiple virtual machines, as required in some instances by a single application.

Applicants respectfully submit claims 1 and 13 would not have been obvious to one of ordinary skill in the art in view of the combination of Bose and Hardin, as

described in detail above with reference to specific limitations of claims 1 and 13. Therefore, claims 1 and 13 are patentable over the prior art. Claims 2-12 and 14-24 depend from respective independent claims 1 and 13 and include further limitations thereon. As such, Applicants respectfully submit that claims 2-12 and 14-24 are allowable over the prior art for the same reasons.
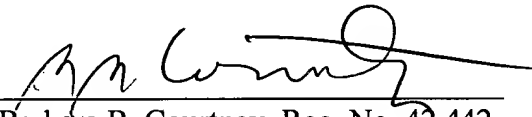
CONCLUSION

In view of the foregoing amendments and remarks, Applicants respectfully submit that claims 1-24 are in condition for allowance. The allowance of the claims is earnestly requested. The Examiner is invited to call the undersigned if there are any issues that remain to be resolved prior to allowance of the claims.

AUTHORIZATION TO CHARGE DEPOSIT ACCOUNT

Please charge deposit account 501914 for any fees due, and not paid herewith, in connection with this Office Action response.

Respectfully submitted,
Shemwell Gregory & Courtney LLP

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Barbara B. Courtney, Reg. No. 42,442
Tel. 408-236-6647

Shemwell Gregory & Courtney LLP
4880 Stevens Creek Blvd., Suite 201
San Jose CA 95129